

B.E./B.Tech.(Full Time) DEGREE END SEMESTER EXAMINATIONS, APR/MAY2012 ELECTRONICS AND COMMUNICATION ENGINEERING BRANCH SECOND SEMESTER – (REGULATIONS 2004) EC 181 – ELECTRONIC DEVICES

Duration: 3 Hours

Max.marks:100

Answer ALL questions

	PART-A (10x2=20 Marks)	
1. 2. 3. 4. 5. 6. 7.	Compare donor and acceptor. Define carrier drift. Define built in potential. Write the cause for diffusion current in a semiconductor. List the high frequency limitations of bipolar devices. Compare BJT and JFET. Why is FET called an unipolar device? Define MOSFET scaling.	
o. 9.	Compare the features of DIAC and TRIAC.	
10.	Give the applications of power bipolar transistor.	
	PART-B (5X16=80 Marks)	
	Distinguish between electron drift current and hole drift current. With schematic arrangement explain how Hall effect can be observed.	(8) (8)
	(i)Explain the forward bias characteristics of PN junction diode. ii)Discuss briefly on transients.	(8) (8)
	OR	
(Describe of the following (i)Schottky diode and its applications (ii)Hetero junctions	(8) (8)
param	(i)Write the Ebers-moll equations for PNP and NPN transistors and leters using any one transistor. (ii)Explain how a BJT can be used as an amplifier.	find DC (10) (6)
	OR	
the eq	(i)Explain how frequency hybrid-Pi model for a transistor configuration uation for transconductance. (ii)Describe the factors which determine the switching speed of a BJT.	and find (10) (6)

14.(a)(i) Explain the construction and working principle of n-channel enhance	cement type
MOSFET.	(12)
(ii) Why are NMOS devices preferred over PMOS devices? Justify y	our answer.
Mono stable multi vibrator	(4)
OB	
OR	
14.(b)Describe the drain and transfer characteristics of JFET and compare	JFET with
MOSFET.	(16)
15.(a)Explain the working principle and the characteristics of UJT and SCR.	(16)
OR	
15.(b)Discuss on the following:	
(i)Power MOSFET	(8)
(ii)Thyristor characteristics	(8)